





interfece Drees	
method Action	hostToCpu(Addr startpc);
method ActionV	<pre>alue#(CpuToHostData) cpuToHost;</pre>
interface MemI	nitlfc iMemInit;
endinterface Meml	nitife ameminit;
<pre>typedef struct {</pre>	
CpuToHostType	c2hType;
Bit#(16) data	;
} CpuloHostData d	eriving(Bits, Eq);
towned Communic	
typedet enum {	ntChar DrintIntlow DrintIntHigh
EXILCODE, Pri	ntenar, PrintintLow, PrintintHigh





















































Loa	ad VC	D		
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	NOW	Load Dump File	×	
	Directory:	afs/athena.mit.edu/user/s/z/szzhang/6.175/lab5/scer	mi/sim — 主 🗎	
	 bdir_dut gtkwave info_dut simdir_dut tbinclude 	Ē out.vcd		
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Oct 14, 2016		http://csg.csail.mit.edu/6.17	'5	T04-31

Wavefor	m View	/er		
GTKWave - /afs/at	thena.mit.edu/user/s/z/s	szzhang/6.175/	'lab5/scemi/sim/out.vcd@vlsi	farm-08 – 🗆 🗙
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Г	Signals	٦r٧	Vaves	6						_
	Time			3300 us					3400 us	
	CLK [Clock] =0									
	pc [Bit#(32)]=000003B0	9	000+	0000039C	000003A0	000003A4	000003A8	000003AC	000003B	0 00
	dInstProbe\$PROBE [-] [DecodedInst]=40702D02F00		387+	387AE00+	0BFB2A4+	OCFB2C4+	087B600+	09FB6CB+	40702D0	0
	dInstProbe\$PROBE iType [IType]=Csrw	4	Csrr		Alu				Csrw	A
	dInstProbe\$PR0BE aluFunc [AluFunc] =Add	1	Add		<u>511</u>	Sr1	Add	0 r	Add	S
	dInstProbe\$PROBE brFunc [BrFunc]=NT	1	NT.	/	~		· · · · · · · · · · · · · · · · · · ·		v	
	dInstProbe\$PROBE dst [-] [Maybe#(Bit#(5))]=00		ZA	2B	20		2D		00	2
	dInstProbe\$PROBE dst tag [Maybe#(Bit#(5))] =Invalid		/alid	(Vee		10-0		Invalid	Ľ
	dinstProbe>PROBE dst Valid [Bit#(5)]=00		DA .	ов	/0C	20	00	Vac	<u>/00</u> /20	0
1	dInstProbe\$PROBE[src1[-] [Maybe#(Blt#(5))]=20		20		ZA	20	20	<u>A</u> ZC	<u>,20</u>	2
1	dInstProbe\$PROBE[SICI]tag [Maybe#(BIt#(5))]=Valid dInstProbe\$PROBE[sic1]Valid [Pit#(5)]=00				VOA.	loc .	00	Voc	Von	20
	dInstProbe\$PROBE src2 [_1 [Maybe#(Bit#(5))]=00		00	02	10	<u>,oc</u>	00	20	100	1
	dInstProbe\$PROBE src2 tag_[Maybe#(Bit#(5))]=00		Inval	id	1.0		00	Valid	Invalid	
	dInstProbe\$PR0BE src2 Valid [Bit#(5)]=00		00	02	10		00	OD	00	1
	dInstProbe\$PROBE csr [-] [Maybe#(Bit#(12))]=1780		C00	102	0010		0000	000D	1780	0
	dInstProbe\$PROBE csr tag [Maybe#(Bit#(12))]=Valid		∕alid		Invalid				Valid	I
	dInstProbe\$PR0BE csr Valid [Bit#(12)]=780		C00	C02	010		000	OOD	780	0
	dInstProbe\$PROBE imm [-] [Maybe#(Bit#(32))]=000000780		OFF+	0FFFFFC+	10000001	0	1000200+	0000000+	0000007	1
	dInstProbe\$PROBE imm tag [Maybe#(Bit#(32))]=Invalid		Inval	id	Valid			Invalid		V
	dInstProbe\$PROBE imm Valid [Bit#(32)]=00000780		FFF+	FFFFFC0B	00000010		00020000	00000000	0000078	0 00











