

EHRs: Designing modules with concurrent methods

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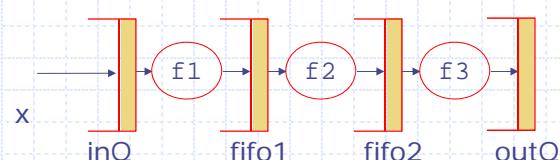
Computer Science & Artificial Intelligence Lab.
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<http://csg.csail.mit.edu/6.375>

L06-1

Elastic pipeline



```
rule stage1 if (True);
    fifo1.enq(f1(inQ.first()));
    inQ.deq();      endrule
rule stage2 if (True);
    fifo2.enq(f2(fifo1.first()));
    fifo1.deq();   endrule
rule stage3 if (True);
    outQ.enq(f3(fifo2.first()));
    fifo2.deq();   endrule
```

Whether these rules can fire concurrently depends crucially on the properties of fifo methods

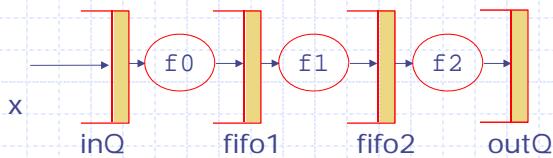
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L06-2

Elastic pipeline

Expressed as a single rule



```
rule elasticPipeline;
    fifo1.enq(f0(inQ.first); inQ.deq;
    fifo2.enq(f1(fifo1.first); fifo1.deq;
    outQ.enq(f2(fifo2.first); fifo2.deq);
endrule
```

What is wrong?

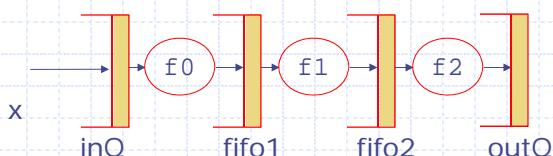
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L06-3

Elastic pipeline

Expressed as a single rule using guard lifting



```
rule elasticPipeline;
    if(inQ.notEmpty && fifo1.notFull)
        begin fifo1.enq(f0(inQ.first); inQ.deq end;
    if(fifo1.notEmpty && fifo2.notFull)
        begin fifo2.enq(f1(fifo1.first); fifo1.deq end;
    if(fifo2.notEmpty && outQ.notFull)
        begin outQ.enq(f2(fifo2.first); fifo2.deq) end;
endrule
```

All stages may be active simultaneously if an enq and deq on a FIFO can be performed simultaneously

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L06-4

Designing FIFOs

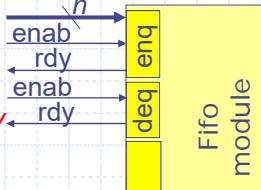
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L06-5

One-Element FIFO

```
module mkCFFFifo (Fifo#(1, t));
    Reg#(t) d <- mkRegU;
    Reg#(Bool) v <- mkReg(False);
    method Action enq(t x) if (!v);
        v <= True; d <= x;
    endmethod
    method Action deq if (v);  
not full  
        v <= False;
    endmethod
    method t first if (v);
        return (d);
    endmethod
endmodule
```



1. What if enq and deq were executed together?
2. Can enq and deq be ready simultaneously?

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L06-6

Two-Element FIFO

```
module mkCFFifo (Fifo#(2, t));
    Reg#(t) da <- mkRegU();
    Reg#(Bool) va <- mkReg(False);
    Reg#(t) db <- mkRegU();
    Reg#(Bool) vb <- mkReg(False);
    method Action enq(t x) if (!vb);
        if va then begin db <= x; vb <= True; end
        else begin da <= x; va <= True; end
    endmethod
    method Action deq if (va);
        if vb then begin da <= db; vb <= False; end
        else begin va <= False; end
    endmethod
    method t first if (va);
        return da;
    endmethod
endmodule
```

Assume, if there is only one element in the FIFO it resides in da

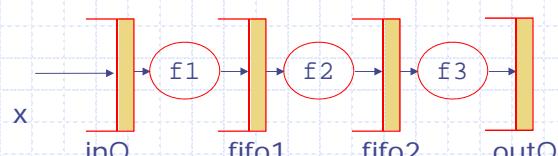


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L06-7

Single-rule version



```
rule stage1;
    if(inQ.notEmpty && fifo1.notFull)
        begin fifo1.enq(f1(inQ.first)); inQ.deq end;
    if(fifo1.notEmpty && fifo2.notFull)
        begin fifo2.enq(f2(fifo1.first)); fifo1.deq end;
    if(fifo2.notEmpty && outQ.notFull)
        begin outQ.enq(f3(fifo2.first)); fifo2.deq end;
endrule
```

This rule is illegal if concurrent operations
on FIFOs are not permitted

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L06-8

Limitations of registers

- ◆ Limitations of a language with only the register primitive
 - No communication between rules or between methods or between rules and methods in the same atomic action i.e. clock cycle
 - Can't express a FIFO with concurrent enq and deq

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L06-9

EHR: Ephemeral History Register

A new primitive element to design modules with concurrent methods

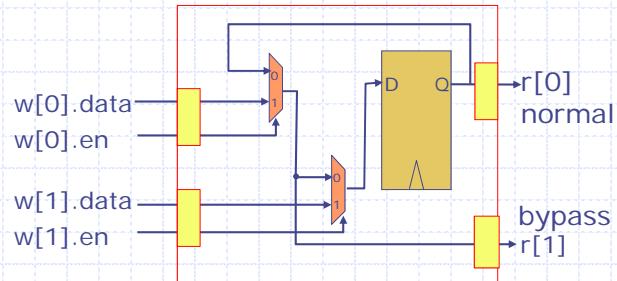
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L06-10

Ephemeral History Register (EHR)

Dan Rosenband [MEMOCODE'04]



$r[1]$ returns:

- the current state if $w[0]$ is not enabled
- the value being written ($w[0].data$) if $w[0]$ is enabled

$w[i+1]$ takes precedence over $w[i]$

$r[0] < w[0]$

$r[1] < w[1]$

$w[0] < w[1] < \dots$

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L06-11

Designing FIFOs using EHRs

- ◆ *Conflict-Free FIFO*: Both enq and deq are permitted concurrently as long as the FIFO is not-full **and** not-empty
 - The effect of enq is not visible to deq, and vice versa
- ◆ *Pipeline FIFO*: An enq into a full FIFO is permitted provided a deq from the FIFO is done simultaneously
- ◆ *Bypass FIFO*: A deq from an empty FIFO is permitted provided an enq into the FIFO is done simultaneously

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L06-12

One-Element Pipelined FIFO

```
module mkPipelineFifo(Fifo#(1, t)) provisos(Bits#(t, tSz));
    Reg#(t) d <- mkRegU;
    Ehr#(2, Bool) v <- mkEhr(False);

    method Action enq(t x) if(!v[1]);
        d <= x;
        v[1] <= True;
    endmethod

    method Action deq if(v[0]);
        v[0] <= False;
    endmethod

    method t first if(v[0]);
        return d;
    endmethod
endmodule
```

Desired behavior
deq < enq
first < deq
first < enq

In any given cycle:
- If the FIFO is not empty
then simultaneous enq and
deq are permitted;
- Otherwise, only enq is
permitted

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L06-13

One-Element Bypass FIFO using EHRs

```
module mkBypassFifo(Fifo#(1, t)) provisos(Bits#(t, tSz));
    Ehr#(2, t) d <- mkEhr(?);
    Ehr#(2, Bool) v <- mkEhr(False);

    method Action enq(t x) if(!v[0]);
        d[0] <= x;
        v[0] <= True;
    endmethod

    method Action deq if(v[1]);
        v[1] <= False;
    endmethod

    method t first if(v[1]);
        return d[1];
    endmethod
endmodule
```

Desired behavior
enq < deq
first < deq
enq < first

In any given cycle:
- If the FIFO is not full then
simultaneous enq and deq
are permitted;
- Otherwise, only deq is
permitted

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L06-14

Two-Element Conflict-free FIFO



```
module mkCFFifo(Fifo#(2, t)) provisos(Bits#(t, tSz));
    Ehr#(2, t) da <- mkEhr(?);
    Ehr#(2, Bool) va <- mkEhr(False);
    Ehr#(2, t) db <- mkEhr(?);
    Ehr#(2, Bool) vb <- mkEhr(False);

    rule canonicalize if(vb[1] && !va[1]);
        da[1] <= db[1]; va[1] <= True;
        vb[1] <= False; endrule

    method Action enq(t x) if(!vb[0]);
        db[0] <= x; vb[0] <= True; endmethod
    method Action deq if(va[0]);
        va[0] <= False; endmethod
    method t first if(va[0]);
        return da[0]; endmethod
endmodule
```

Assume, if there is only one element in the FIFO it resides in da

Desired behavior
enq CF deq
first < deq
first CF enq

In any given cycle:
- Simultaneous enq and deq are permitted only if the FIFO is not full and not empty

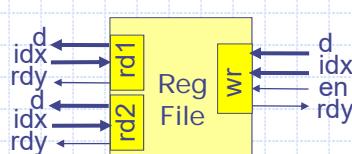
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L06-15

Register File:

normal and bypass



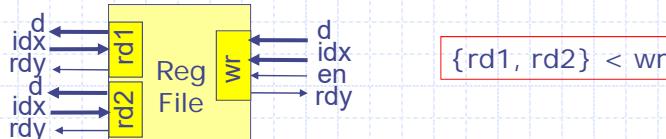
- ◆ Normal rf: the effect of a register update can only be seen a cycle later, consequently, reads and writes are conflict-free
 - $\{rd1, rd2\} < wr$
- ◆ Bypass rf: in case of concurrent reads and write, check if $rd1 == wr$ or $rd2 == wr$ then pass the new value as the result and update the register file, otherwise the old value in the rf is read
 - $wr < \{rd1, rd2\}$

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L06-16

Normal Register File



```
module mkRFile(RFile);
    Vector#(32,Reg#(Data)) rfile <- replicateM(mkReg(0));

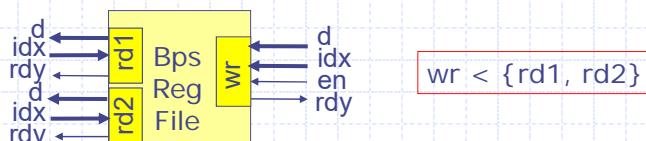
    method Action wr(Rindex rindx, Data d);
        rfile[rindx] <= d;
    endmethod
    method Data rd1(Rindex rindx) = rfile[rindx];
    method Data rd2(Rindex rindx) = rfile[rindx];
endmodule
```

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L06-17

Bypass Register File using EHR



```
module mkBypassRFile(RFile);
    Vector#(32,EHR#(2, Data)) rfile <-
        replicateM(mkEHR(0));

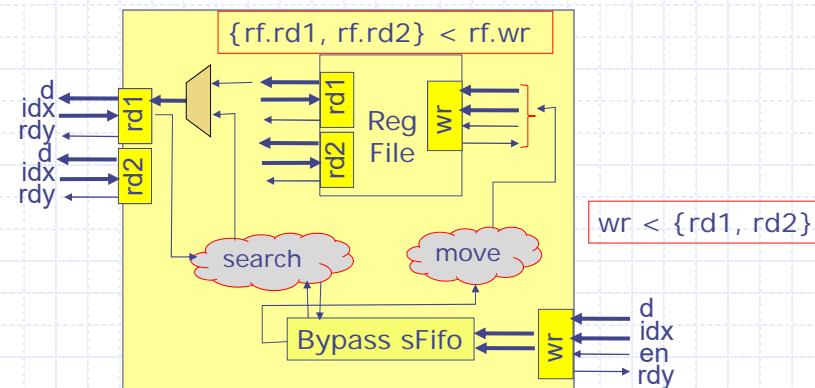
    method Action wr(Rindex rindx, Data d);
        rfile[rindx][0] <= d;
    endmethod
    method Data rd1(Rindex rindx) = rfile[rindx][1];
    method Data rd2(Rindex rindx) = rfile[rindx][1];
endmodule
```

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Bypass Register File with external bypassing



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L06-19

Bypass Register File with external bypassing

```
module mkBypassRFile(BypassRFile);
    RFile                  rf   <- mkRFile;
    Fifo#(1, Tuple2 #(RIndex, Data))      bypass   <- mkBypassSFifo();
    rule move;
        begin rf.wr(bypass.first); bypass.deq end;
    endrule
    method Action wr(RIndex rindex, Data d);
        bypass.enq(tuple2(rindex, d));
    endmethod
    method Data rd1(RIndex rindex) =
        return (!bypass.search1(rindex)) ? rf.rd1(rindex)
                                         : bypass.read1(rindex);
    method Data rd2(RIndex rindex) =
        return (!bypass.search2(rindex)) ? rf.rd2(rindex)
                                         : bypass.read2(rindex);
    endmodule
```

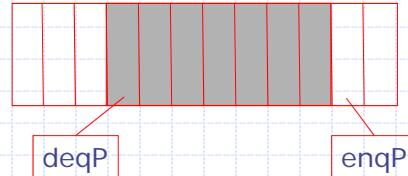
A red box highlights the condition $\{rf.rd1, rf.rd2\} < rf.wr$. Another red box highlights the condition $wr < \{rd1, rd2\}$.

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L06-20

N-element Conflict-free FIFO



- ◆ Checking for emptiness and fullness
 - Empty: $\text{enqP} == \text{deqP}$
 - Full: $\text{enqP} == \text{deqP} + \text{FIFO_size}$
- ◆ To deal with the wrap around problem, assume enqP and deqP can contain indices for up to twice the size of the FIFO. Pointers are incremented modulo $2 * n$ while buffer is accessed by pointer modulo n values

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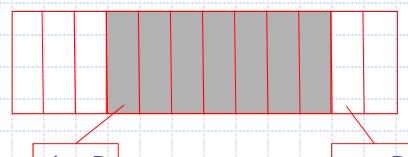
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L06-21

N-element Conflict-free FIFO

locks for preventing accesses until canonicalization

enqEn deqEn



- ◆ an enq updates enqP and puts the data into the array. It also sets enqEn to false to prevent further enqueues
- ◆ a deq updates deqP and sets deqEn to false to prevent further dequeues
- ◆ Canonicalize rule calculates the new count and sets the enqEn and deqEn bits appropriately

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L06-22

N-element Conflict-free FIFO methods

```
method Action enq(t x) if(enqEn[0]);
    enqP[0] <= (enqP[0] + 1)%n2; // n2 is 2*nb
    enqEn[0] <= False;
    d[enqP[0]%nb] <= x;
endmethod

method Action deq if(deqEn[0]);
    deqP[0] <= (deqP[0] + 1)%n2;
    deqEn[0] <= False;
endmethod

method t first if(deqEn[0]);
    return d[deqP[0]%nb];
endmethod

endmodule
```

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L06-23

N-element Conflict-free FIFO: The canonicalization rule

```
rule canonicalize;
    let cnt = enqP[1] >= deqP[1]?
        enqP[1] - deqP[1]:
        (enqP[1]%nb + nb) - deqP[1]%nb;
    if(!enqEn[1] && cnt != nb) enqEn[1] <= True;
    if(!deqEn[1] && cnt != 0) deqEn[1] <= True;
endrule
```

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L06-24