

---

# CSAIL

Computer Science and Artificial Intelligence Laboratory

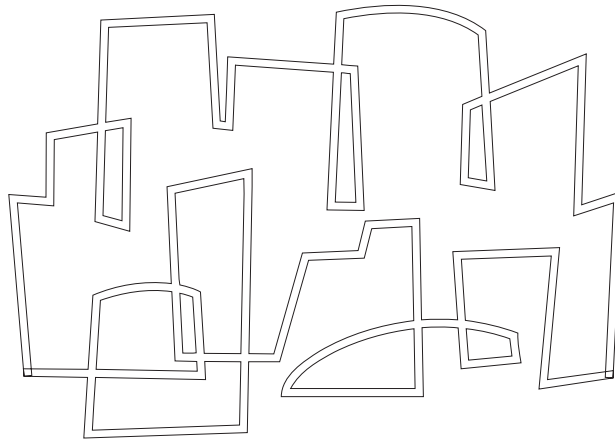
Massachusetts Institute of Technology

## Specification of Memory Models and Design of Provably Correct Cache Coherence Protocols

Xiaowei Shen, Arvind

1997, January

Computation Structures Group  
Memo 398



The Stata Center, 32 Vassar Street, Cambridge, Massachusetts 02139

---