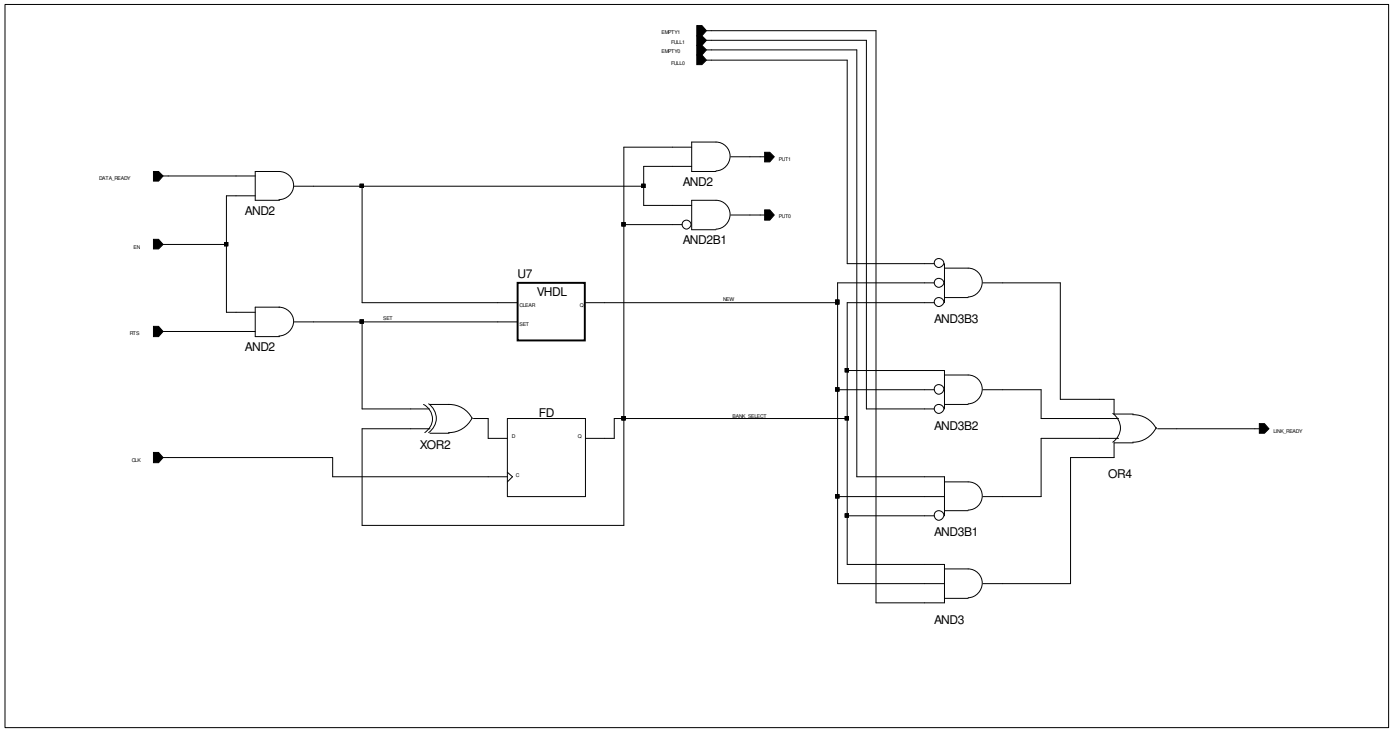
EMPTY1
FULL1
EMPTY0
FULL0
DATA_READY
AND2
EN
RTS
AND2
AND2
PUT1
AND2B1
PUT0
U7
VHDL
CLEAR
SET
Q
NEW
SET
FD
D
Q
C
XOR2
BANK_SELECT
CLK
AND3B3
AND3B2
AND3B1
AND3
OR4
LINK_READY