
CSAIL

Computer Science and Artificial Intelligence Laboratory

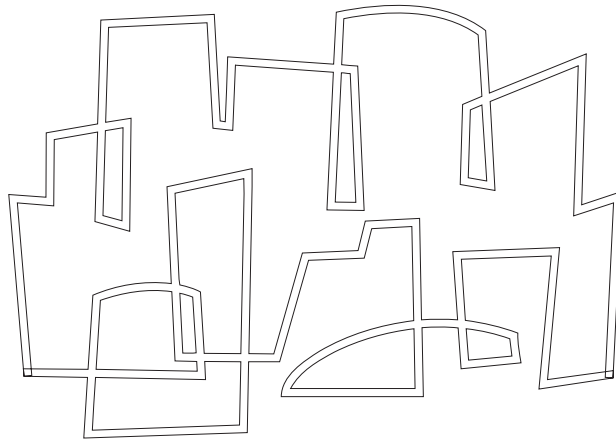
Massachusetts Institute of Technology

Intelligent SRAM (ISRAM) for Improved Embedded System Performance

Prabhat Jain, Ed Suh, Srinivas Devadas

In the proceedings of the
40th Design Automation Conference, June 2003

Computation Structures Group
Memo 459



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