
CSAIL

Computer Science and Artificial Intelligence Laboratory

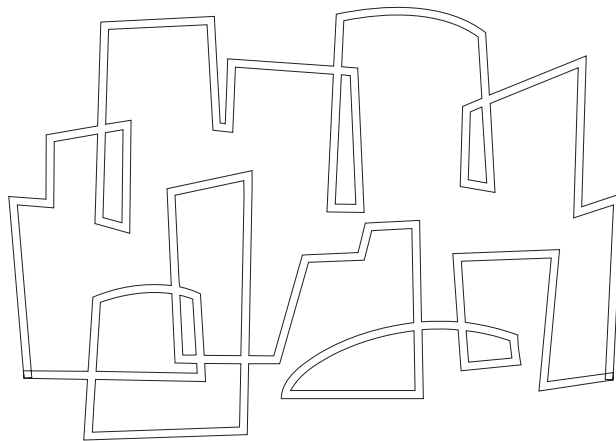
Massachusetts Institute of Technology

AEGIS: Architecture for Tamper-Evident and Tamper-Resistant Processing

Edward Suh, Dwaine Clarke, Blaise Gassend,
Marten van Dijk, Sridhar Devadas

Submission to the Proceedings of the
17th International Conference on Supercomputing

Computation Structures Group
Memo 461



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