## **CSAIL**

Computer Science and Artificial Intelligence Laboratory

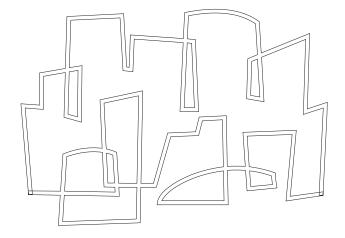
Massachusetts Institute of Technology

## Efficient Memory Integrity Verification and Encryption for Secure Processors

G. Edward Suh, Dwaine Clarke, Blaise Gassend, Marten van Dijk, Srini Devadas

To appear in the Proceedings of the 36th Annual International Symposium on Microarchitecture, December 2003

## Computation Structures Group Memo 465



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