
CSAIL

Computer Science and Artificial Intelligence Laboratory

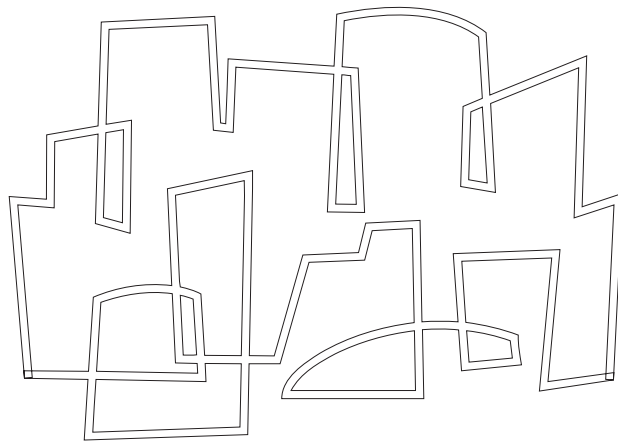
Massachusetts Institute of Technology

Design and Implementation of a Single-Chip Secure Processor Using Physical Random Functions

Computation Structures Group
Memo 483

November, 2004

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